

IN THE UNITED STATES PATENT AND TRADEMARK OFFICE

In re PATENT application of

SHUNPEI YAMAZAKI et al

Serial No. 08/691,434

Filed: 08/02/1996

For: METHOD OF FABRICATING

SEMICONDUCTOR DEVICES AND

APPARATUS FOR PROCESSING A

SEMICONDUCTOR



Group Art Unit: 2822

Examiner: M. Wilczewski

**CERTIFICATE OF MAILING**

I hereby certify that this correspondence is being deposited with the United States Postal Service with sufficient postage as First Class Mail in an envelope addressed to: Assistant Commissioner for Patents, Washington, D.C. 20231, on 11-10-99

Carrie M. Sixbey

**INFORMATION DISCLOSURE STATEMENT**

Honorable Assistant Commissioner of Patents

Washington, D.C. 20231

Sir:

In accordance with the provisions of 37 C.F.R. 1.56 and 37 C.F.R. 1.97-1.99, Applicant submits herewith attached Form PTO-1449 listing references known to Applicant and requests that these references be made of record in the above identified application. Copies of the references listed are submitted herewith in accordance with 37 C.F.R. 1.98(a).

The Commissioner is hereby authorized to charge fees under 37 C.F.R. §§1.16, 1.17, 1.20(a), 1.20(b), 1.20(c), and 1.20(d) (except the Issue Fee) which may be required now or hereafter, or credit any overpayment to Deposit Account No. 19-2380. A duplicate copy of this sheet is attached.

Respectfully submitted,

  
Eric J. Robinson

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